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(16 AND 4).PGPB,USPT.	2
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DB =	PGPB,USPT; PLUR=YES; OP=OR		
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<u>L33</u>	14 and 115	2	<u>L33</u>
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<u>L31</u>	14 and 113	47	<u>L31</u>
<u>L30</u>	14 and 112	76	<u>L30</u>
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<u>L28</u>	18 and 116	1	<u>L28</u>
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<u>L26</u>	18 and 114	14	<u>L26</u>
<u>L25</u>	18 and 113	14	<u>L25</u>
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17 and 116	22	<u>L22</u>
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(711/170-173)![CCLS]	3672	<u>L15</u>
(712/2-25, 225-227, 300)[CCLS]	2866	<u>L14</u>
(712/2-25, 225-227, 300)![CCLS]	2866	<u>L13</u>
(712/2-300)![CCLS]	12886	<u>L12</u>
(712/2-300)[CCLS]	12886	<u>L11</u>
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L8 and l4	7	<u>L10</u>
L7 and l4	. 8	<u>L9</u>
L7 and (offset\$1) and (length or width or size)	163	<u>L8</u>
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L2 and radix	1	<u>L3</u>
6898691.pn.	1	<u>L2</u>
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AbstractPlus | Full Text: PDF(168 KB) IEEE CNF

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	<ol> <li>Statistics for Parallelism and Abstraction Level in Digital Simulation Soule, L.; Blank, T.;</li> <li>Design Automation, 1987. 24th Conference on 28-1 June 1987 Page(s):588 - 591</li> </ol>
	AbstractPlus   Full Text: PDF(416 KB) IEEE CNF Rights and Permissions
	7. The offline software framework of the Pierre Auger Observatory Argiro, S.; Nellen, L.; Paul, T.C.; Porter, T.; Prado, L., Jr.; Nuclear Science Symposium Conference Record, 2004 IEEE Volume 3, 16-22 Oct. 2004 Page(s):1862 - 1866 Vol. 3 Digital Object Identifier 10.1109/NSSMIC.2004.1462607
•	AbstractPlus   Full Text: PDF(947 KB) IEEE CNF Rights and Permissions
	8. GASP: A programmable signal processor Rogers, J.; Acoustics, Speech, and Signal Processing, IEEE International Conference on I Volume 1, Apr 1976 Page(s):651 - 651 AbstractPlus   Full Text: PDF(91 KB) IEEE CNF Rights and Permissions
	9. Characterizing the impact of different memory-intensity levels
· ·	Kotla, R.; Devgan, A.; Ghiasi, S.; Keller, T.; Rawson, F.;  Workload Characterization, 2004. WWC-7. 2004 IEEE International Workshop 25 Oct. 2004 Page(s):3 - 10  Digital Object Identifier 10.1109/WWC.2004.1437388  AbstractPlus   Full Text: PDF(421 KB) IEEE CNF  Rights and Permissions
Lucina	10. Energy-efficient instruction set synthesis for application-specific proces: Jong-eun Lee; Kiyoung Choi; Dutt, N.D.; Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 20( Symposium on 25-27 Aug. 2003 Page(s):330 - 333 Digital Object Identifier 10.1109/LPE.2003.1231889
	AbstractPlus   Full Text: PDF(532 KB) IEEE CNF Rights and Permissions
	11. Improving dynamic cluster assignment for clustered trace cache process Ravi Bhargava; John, L.K.; Computer Architecture, 2003. Proceedings. 30th Annual International Sympos 9-11 June 2003 Page(s):264 - 274 Digital Object Identifier 10.1109/ISCA.2003.1207006
	AbstractPlus   Full Text: PDF(295 KB)   IEEE CNF   Rights and Permissions
	12. Breaking an application specific instruction-set processor: the first step embedded software testing Dielissen, J.T.M.H.; Otero Mathijssen, B.L.; Huisken, J.A.; High-Level Design Validation and Test Workshop, 2002. Seventh IEEE Interna 27-29 Oct. 2002 Page(s):89 - 92
	AbstractPlus   Full Text: PDF(372 KB) IEEE CNF Rights and Permissions
	13. Power-efficient value speculation for high-performance microprocessors

Moreno, R.; Pinuel, L.; del Pino, S.; Tirado, F.; Euromicro Conference, 2000. Proceedings of the 26th Volume 1, 5-7 Sept. 2000 Page(s):292 - 299 vol.1 Digital Object Identifier 10.1109/EURMIC.2000.874645 AbstractPlus | Full Text: PDF(660 KB) IEEE CNF Rights and Permissions 14. A methodology for accurate performance evaluation in architecture explicit Hadjiyiannis, G.; Russo, P.; Devadas, S.; Design Automation Conference, 1999. Proceedings. 36th 21-25 June 1999 Page(s):927 - 932 Digital Object Identifier 10.1109/DAC.1999.782230 AbstractPlus | Full Text: PDF(556 KB) IEEE CNF Rights and Permissions 15. Value prediction in VLIW machines Nakra, T.; Gupta, R.; Soffa, M.L.; Computer Architecture, 1999. Proceedings of the 26th International Symposium 2-4 May 1999 Page(s):258 - 269 Digital Object Identifier 10.1109/ISCA.1999.765956 AbstractPlus | Full Text: PDF(212 KB) IEEE CNF Rights and Permissions 16. Execution characteristics of object oriented programs on the UltraSPAR( Radhakrishnan, R.; John, L.; High Performance Computing, 1998. HIPC '98. 5th International Conference C 17-20 Dec. 1998 Page(s):202 - 211 Digital Object Identifier 10.1109/HIPC.1998.737990 AbstractPlus | Full Text: PDF(224 KB) IEEE CNF Rights and Permissions 17. Carrier specific routing for international switched transit service Chiang, R.; Pel, E.T.; Communication Technology Proceedings, 1996. ICCT'96., 1996 International ( Volume 2, 5-7 May 1996 Page(s):866 - 869 vol.2 Digital Object Identifier 10.1109/ICCT.1996.545018 AbstractPlus | Full Text: PDF(304 KB) IEEE CNF Rights and Permissions 18. Performance improvement of the memory hierarchy of RISC-systems by **D-technology** Kleiner, M.B.; Kuhn, S.A.; Weber, W.; Electronic Components and Technology Conference, 1995. Proceedings., 45th 21-24 May 1995 Page(s):645 - 655 Digital Object Identifier 10.1109/ECTC.1995.515351 AbstractPlus | Full Text: PDF(1024 KB) IEEE CNF Rights and Permissions 19. A GaAs 32-bit RISC microprocessor Harrington, D.L.; Troeger, G.L.; Gee, W.C.; Bolen, J.A.; Vogelsang, C.H.; Nica C.M.; Roh, Y.K.; Nguyen, K.Q.; Fay, J.F.; Reeder, J.; Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 1988. Technical Di Annual IEEE 6-9 Nov. 1988 Page(s):87 - 90 Digital Object Identifier 10.1109/GAAS.1988.11030 AbstractPlus | Full Text: PDF(248 KB) IEEE CNF Rights and Permissions

20. A virtual environment for real-time interaction with robotic systems

Shepperson, A.P.; Zalzala, A.M.S.; <u>Innovations in Manufacturing Control Through Mechatronics, IEE Colloquium (22 Nov 1995 Page(s):8/22 - 8/25</u>
AbstractPlus   Full Text: PDF(268 KB) IEE CNF

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